

PATENT ABSTRACTS OF JAPAN

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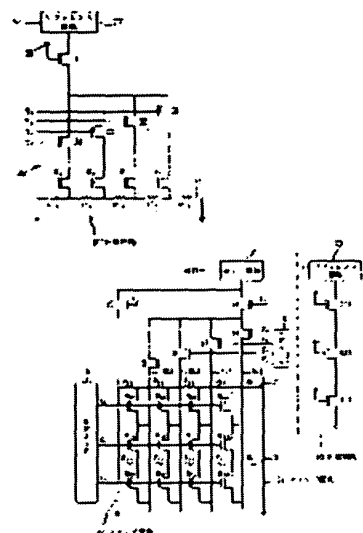
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(54) SENSE AMPLIFIER CIRCUIT DEVICE

(57)Abstract:

PURPOSE: To provide the operation of a sensing circuit with a margin by providing a reference cell and a selecting means in a reference voltage generating part to impress controlled voltage on a sensing part.

CONSTITUTION: A parasitic resistance value for parasitizing on the source side of a memory cell transistor M00 and so on provided in a memory cell array parasitizes the same resistance value as that of the memory cell transistor connected to respective bit lines also on a reference transistor M0 provided in an external circuit 10' and besides, the reference transistor corresponding to a position where the selected memory cell transistor is arranged can be selected by NMOS transistors 31 to 34. A current to flow in the memory cell transistor M00 and so on and the current to flow the reference transistor M0 and so on of the external circuit 10' are same even when either memory cell transistor M00 and so on are selected, and the same memory current can be supplied to a sensing circuit 7 and a reference circuit 20'. Thus, an allowance can be added to the reading operation margin of the sensing circuit.



LEGAL STATUS

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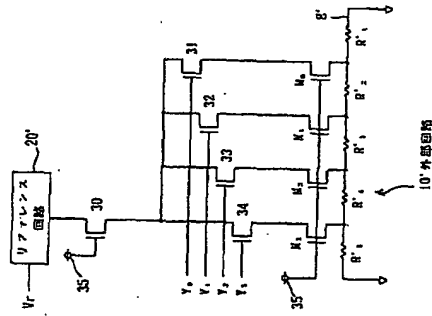
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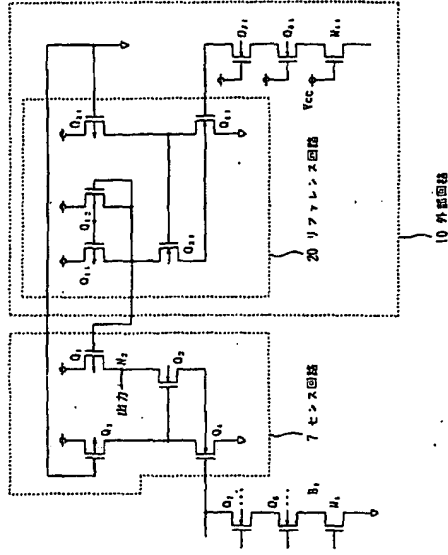
特開平4-67500 (7)

第1図



特開平4-67500 (8)

第3図



第2図

